

Call for Papers

The Sixteenth International Workshop on Assurance in Distributed Systems and Networks (ADSN2017)

Atlanta, Georgia, USA June 5 - 8, 2017

Sponsored by the IEEE Computer Society Technical Committee on Distributed Processing in cooperation with the IEICE Communications Society Technical Committee on Information Networks and the IEICE Information and Systems Society Technical Committee on Dependable Computing (approval pending).

ADSN 2017 will be held in conjunction with the 37th International Conference on Distributed Computing Systems (ICDCS 2017), Atlanta, Georgia, USA, June 5 - 8, 2017. (see for details <https://icdcs2017.gatech.edu/>).

ADSN 2017 is the sixteenth edition of this International Workshop demonstrating its long-term continuity and continuously received recognition. The objective of the Workshop is to provide an effective forum for original scientific and engineering advances in Assurance in Distributed Systems and Networks. Along with recent rapid growth of the Internet and ubiquitous networks, applications for autonomous decentralized systems are emerging in many important areas like transportation, automotive systems, smart energy grids and the Internet of Things (IoT) to name a few. In these distributed systems and networks, heterogeneous requirements are independently generated and the requirements themselves are frequently changing. Assurance in these distributed systems and networks is defined as capability of guaranteeing functional and non-functional system properties such as dependability, security, timeliness and adaptivity in heterogeneous and changing environments. The workshop theme is critical in meeting ever more demanding requirements for assurance in systems and networks. Technologies supporting assurance including integration of various fields such as real time, fault tolerance, autonomy, mobility and intelligence will have to be incorporated in complex distributed systems and networks.

Topics of interest for this workshop include, but are not limited to:

- Assurance technologies for new generation networks
- Assurance in cooperative embedded, grid, and cloud computing systems
- Assurance in service-oriented and web-service based computing systems
- Critical infrastructure protection
- Heterogeneous systems coexistence and consistency technologies
- Network control (QoS, QoE, CoS, etc) technologies for assurance
- Self-stabilization and sensor networks assurance
- Agents technologies for assurance
- Assurance in safety-critical, automotive, and avionic systems
- Software and hardware technologies for assurance in intelligent transportation systems, train control, and space computers
- System life-cycle assurance in specification, design, implementation, testing, verification, and validation
- Assurance in IoT systems
- Security technologies for assurance
- System management and proactive fault management technologies for assurance
- Technologies for achieving high assurance in complex systems

Papers and Submissions

Papers must be written in English, and they have to be Postscript, PDF, or Word format. The complete manuscript should be no more than 6 pages following IEEE conference proceedings style and guidelines. The following information must be provided as well:

- (1) paper title, full name, affiliation of the author(s)
- (2) full name, affiliation, complete address, phone number and email address of the author to be contacted, as well as
- (3) an abstract (up to 150 words).

All paper submissions will be handled electronically by the EasyChair system. Papers should be submitted via EasyChair system.

<https://easychair.org/conferences/?conf=adsn2017>

Authors are expected to present their paper at the workshop. At least one author of each paper must register for ICDCS to be included in the workshop program.

Important Dates

Deadline for submission: **Feb 10, 2017**

Acceptance notification: Mar 25, 2017

Camera Ready Deadline: April 10, 2017

General Chair

Mirosław Malek, USI-Lugano,
Switzerland

Vice Chair

Yoshiaki Kakuda, Hiroshima City
University, Japan

Program Chair

Karthik Pattabiraman, University of
British Columbia, Canada

Vice Program Chairs

Felicita Di Giandomenico,
ISTI-CNR, Italy

Eitaro Kohno, Hiroshima City
University, Japan

Harigovind V. Ramasamy, IBM,
USA

Program Committee

Masaki Aida, Tokyo Metropolitan
University, Japan

Emmanuelle Anceaume, CNRS / IRISA,
France

Robin Berthier, Univ. of Illinois at
Urbana-Champaign, USA

Shuo Chen, Microsoft Research, USA

Jorge Cobb, Univ. of Texas at Dallas,
USA

Elias P. Duarte Jr., Federal Univ. of
Parana, Brazil

Paul Ezhilchelvan, Newcastle University,
UK

Junichi Funasaka, Hiroshima City
University, Japan

Karl M. Goeschka, Vienna University of
Technology, Austria

Aniruddha Gokhale, Vanderbilt
University, USA

Anatoliy Gorbenko, National Aerospace
University, Ukraine

Istvan Majzik, Budapest University of
Technology and Economics, Hungary

Yoshifumi Manabe, Kogakuin University,
Japan

Yukikazu Nakamoto, University of
Hyogo, Japan

Krerak Piromsopa, Chulalongkorn
University, Thailand

Alexander Romanovsky, Newcastle
University, UK

Elena Troubitsyna, Abo Akademi,
Finland

Tatsuhiro Tsuchiya, Osaka University,
Japan

Marco Vieira, Univ of Coimbra, Portugal

Yufeng Wang, Nanjing Univ. of Posts
and Telecommunications, China

Xin Wang, Fudan University, China

Charles Weinstock, Software
Engineering Institute, USA